RZ/A1H Group

JPEG Codec Unit(JCU) Sample Driver

R01AN1862EJ0100
Rev.1.00
Jun.20.2014

Introduction

This application note describes the sample driver which is decoded from the JPEG image data, and encoded to the JPEG image data.

The JPEG Codec Unit(JCU) sample driver offers the following features:

- The JPEG image data is converted to a raw image data of the RGB565, ARGB8888, and YCbCr422 formats.
- The raw image data of the YCbCr format is converted to a JPEG image data.
- The interrupt information is used for notice of conversion completion.

Target Device

RZ/A1H

When applying the sample program covered in this application note to another microcomputer, modify the program according to the specifications for the target microcomputer and conduct an extensive evaluation of the modified program.

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1. Specifications

Table 1.1 lists the Peripheral Functions and Their Applications, and Figure 1.1 shows the Operation Overview.

Table 1.1 Peripheral Functions to be Used and their Uses

Peripheral functions	Uses
JPEG Codec Unit(JCU)	Converts image data.
Interrupt controller(INTC)	The processor will receive interrupts when decoding or encoding is completed, failed, or paused.
Serial Communication Interface with FIFO(SCIF) Ch2	Output sample code message.

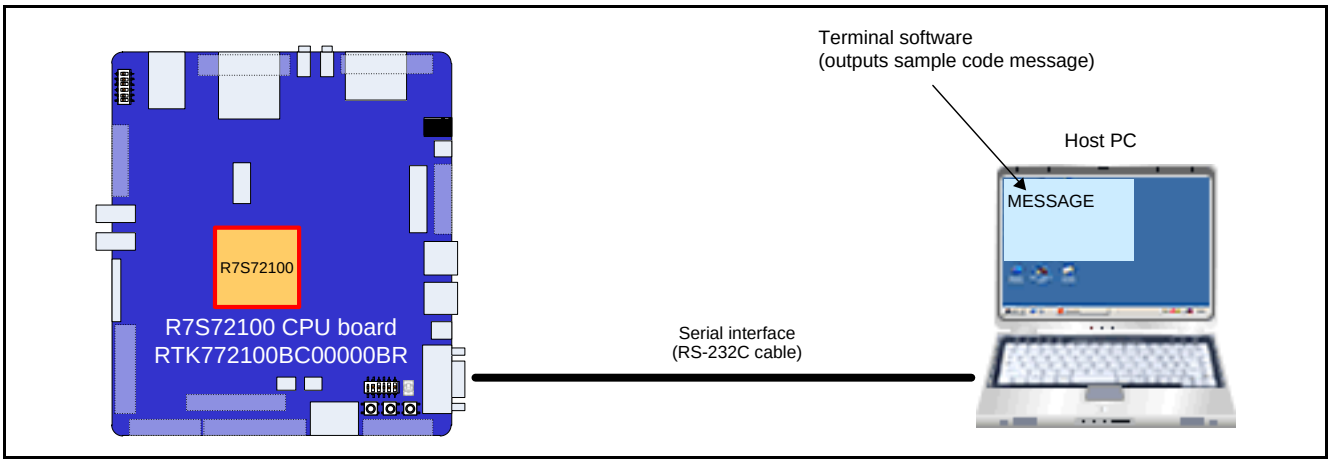

Figure 1.1 **Operation Overview**

2. Operation Check Conditions

The sample code accompanying this application note has been run and confirmed under the conditions below.

Table 2.1 Operation Check Conditions

Item	Contents
MCU used	RZ/A1H
Operating frequency*	CPU clock (Iϕ): 400MHz Image processing clock (Gϕ): 266.67MHz Internal bus clock (Bϕ): 133.33MHz Peripheral clock 1 (P1ϕ): 66.67MHz Peripheral clock 0 (P0ϕ): 33.33MHz
Operating voltage	Power supply voltage (I/O): 3.3V Power supply voltage (Internal): 1.18V
Integrated development environment	ARM® integrated development environment ARM Development Studio 5 (DS-5™) Version 5.16
C compiler	ARM C/C++ Compiler/Linker/Assembler Ver.5.03 [Build 102]
	Compiler options (excluding additional directory path) -O3 -Ospace --cpu=Cortex-A9 --littleend --arm --apcs=/interwork --no_unaligned_access --fpu=vfpv3_fp16 -g
Operating mode	Boot mode 0 (CS0-space 16-bit booting)
Communication setting of terminal software	• Communication speed: 115200bps • Data length: 8 bits • Parity: None • Stop bit length: 1 bit • Flow control: None
Board used	GENMAI Board • RTK772100BC00000BR (R7S72100 CPU board) • RTK77210000B00000BR (R7S72100 Option board)
Device used	LCD. (Only the sample should be used.) • Serial interface (D-sub 9-pin connector)

3. Reference Application Note(s)

For additional information associated with this document, refer to the following application note(s).

RZ/A1H Example of Initialization (R01AN1646EJ)
RZ/A1H Definition of I/O Register file “iodefine.h” (R01AN1860JJ)
RZ/A1H Group OS porting layer “OSPL” Sample Program (R01AN1887EJ)

4. Peripheral Functions

The basic functions of the JCU are described in the RZ/A1H Group User's Manual: Hardware.

5. Description of Hardware

5.1 Hardware Configuration

Figure 5-1 shows examples of hardware devices connected.

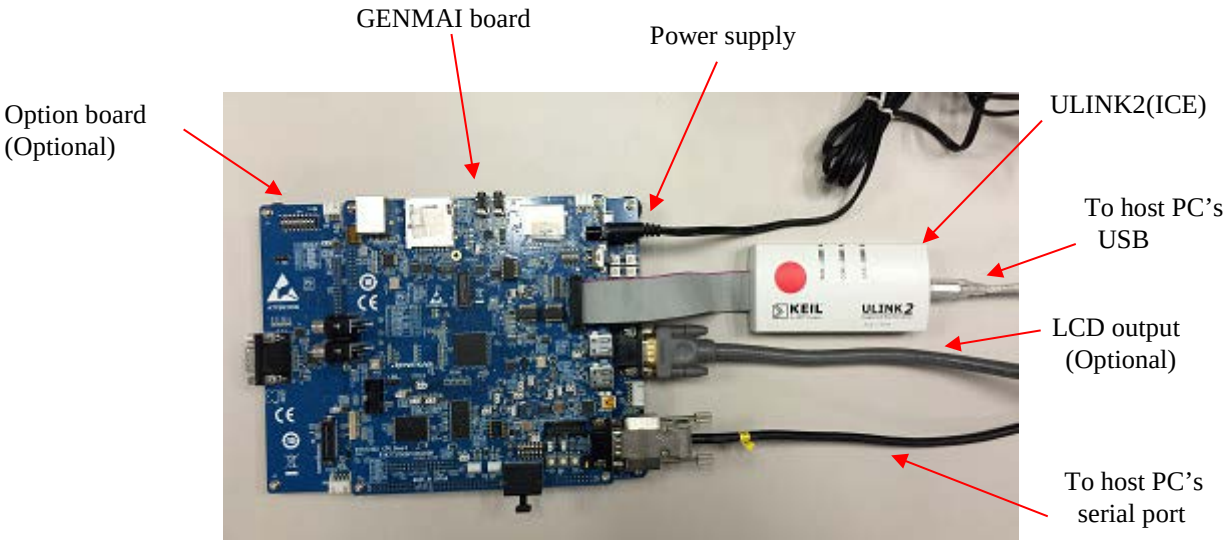

Figure 5-1 Examples of Hardware Devices Connected

5.2 List of Pins to be Used

Table 5-1 lists the pins to be used and their functions.

Table 5-1 Pins to be Used and their Functions

Pin name	I/O	Description
None		

6. Description of Software

6.1 Operation Outline

Figure 6-1 shows the sequence of image data converted using the synchronous function.

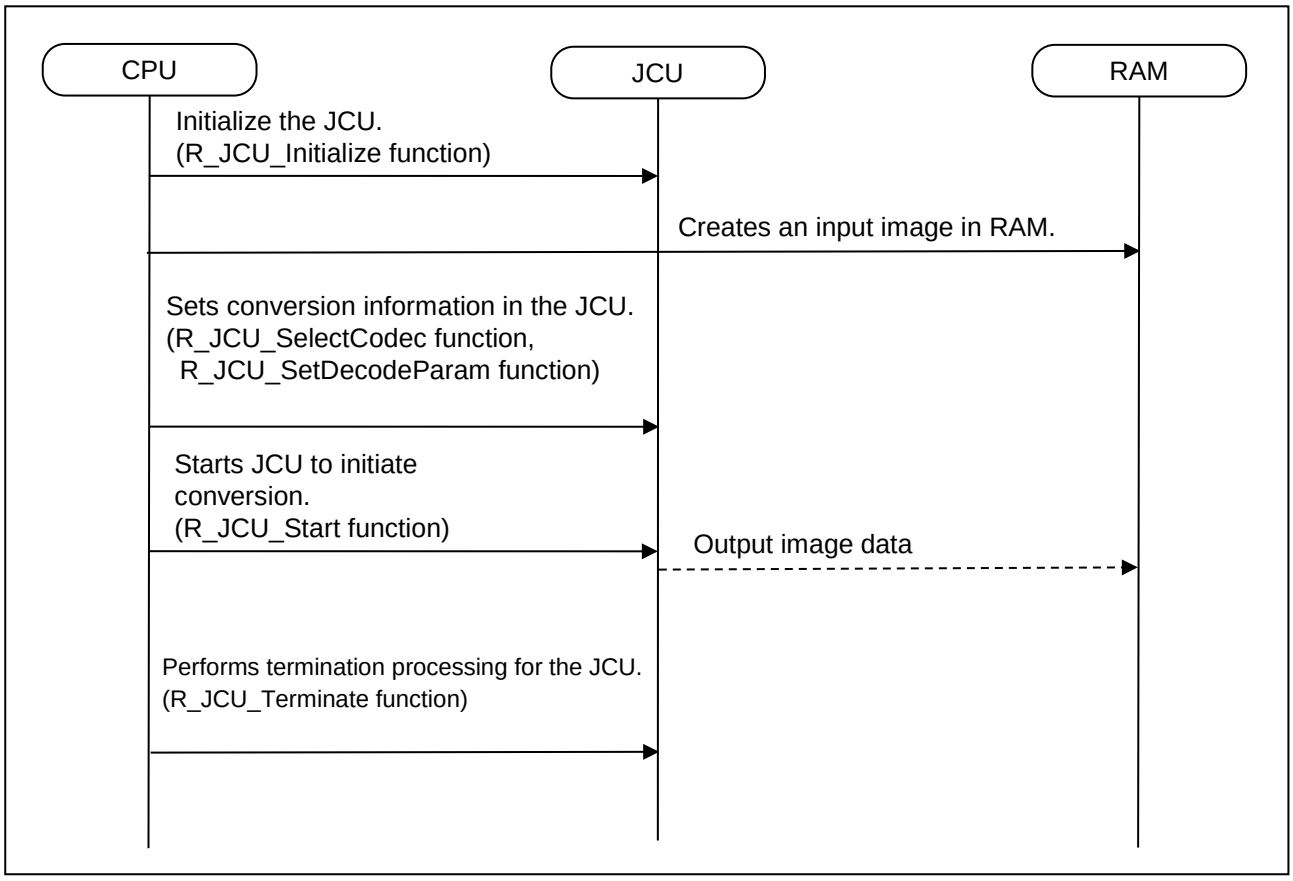

Figure 6-1 Sequence of image data conversion(using the synchronous function).

Please refer to OS transplantation layer OSPL for RZ/A1H group PFV,JCU (R01AN1887EJ) , when using the asynchronous function.

This sample program has processing of 3 kinds, "decoding processing of a JPEG picture"(R_JCU_SampleDecode function), "decoding and encoding processing of a JPEG picture"(R_JCU_SampleDecodeEncode function) and "the processing indicated after decoding of a JPEG picture"(R_JCU_SampleDecodeAndShow function).

6.1.1 Preparations

The following preparations in Sample Code.

1. Terminal software is started in a host PC and it's established as follows. (In the case of Tera Term)

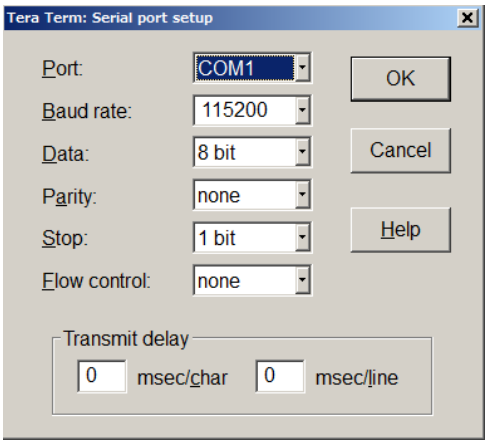

Figure 6.2 Setting of a serial port

2. When a sample program is executed, a message is output at a terminal as follows.

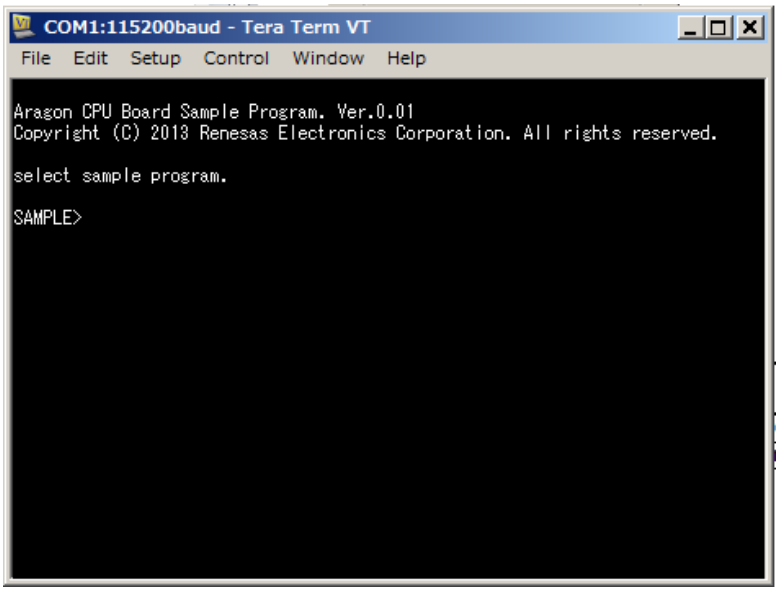

Figure 6.3 Message output at sample program execution

6.2 Memory Mapping

Figure 6.4 shows the Address Space of the RZ/A1H group and the Memory Mapping of the GENMAI Board RTK772100BC00000BR.

In this sample code, the code and data used in the ROM area is located in the NOR flash memory connected to the CS0 space, and the code and data used in the RAM area is located in the large-capacity on-chip RAM.

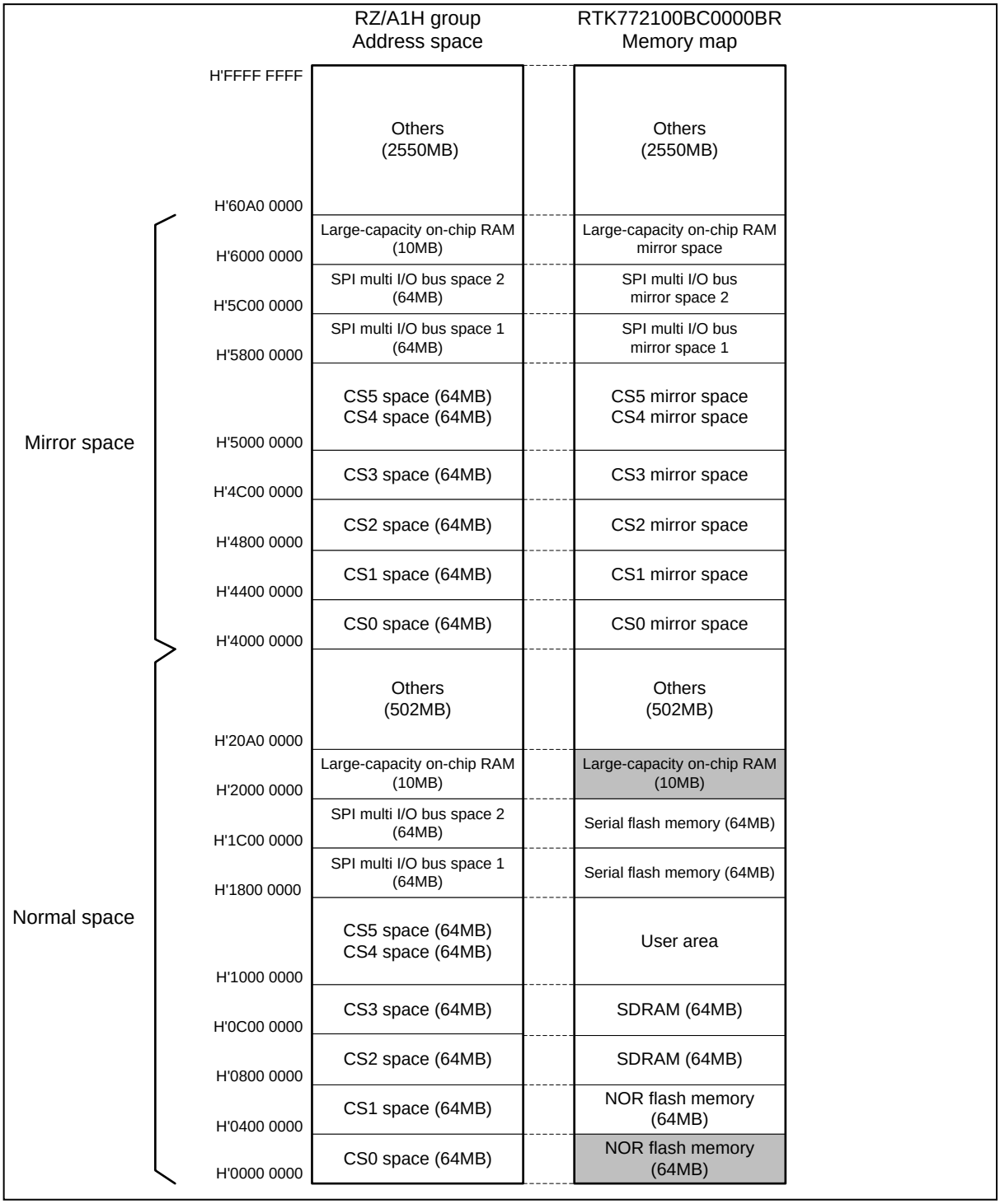

Figure 6.5 Memory Mapping

6.2.1 Section Assignment in Sample Code

In this sample code, the exception processing vector table and the IRQ interrupt handler are assigned to the large-capacity on-chip RAM, and they are executed in such RAM to speed up the interrupt processing. The transfer processing from the NOR flash memory area which is the program code of the exception processing vector table and the IRQ interrupt handler to the large-capacity on-chip RAM area, the clear to zero processing for the data selection without initial data, and the initialization for the data selection with initial data are executed by using the scatter-loading function. Refer to "Image structure and generation" in "ARM Compiler toolchain Using the Linker" provided by the ARM for more information about the scatter-loading function.

Table 6.1 and Table 6.2 list the Sections to be Used in this sample code. Figure 6.6 shows the Section Assignment for the initial condition of the sample code and the condition after using the scatter-loading function.

Table 6.1 Sections to be Used (1/2)

Area Name	Description	Type	Loading Area	Execution Area
VECTOR_TABLE	Exception processing vector table	Code	FLASH	FLASH
RESET_HANDLER	Program code area of reset handler processing This area consists of the following sections. • INITCA9CACHE (L1 cache setting) • INIT_TTB (MMU setting) • RESET_HANDLER (Reset handler)	Code	FLASH	FLASH
CODE_BASIC_SETUP	Program code area to optimize operating frequency and flash memory	Code	FLASH	FLASH
InRoot	This area consists of the sections located in the root area such as C standard library.	Code and RO Data	FLASH	FLASH
CODE_FPU_INIT	Program code area for NEON and VFP initializations This area consists of the following sections. • CODE_FPU_INIT • FPU_INIT	Code	FLASH	FLASH
CODE_RESET	Program code area for hardware initialization This area consists of the following sections. • CODE_RESET (Startup processing) • INIT_VBAR (Vector base setting)	Code	FLASH	FLASH
CODE_IO_REGRW	Program code area for read/write functions of I/O register	Code	FLASH	FLASH
CODE	Program code area for defaults All the Code type sections which do not define section names with C source are assigned in this area.	Code	FLASH	FLASH
CONST	Constant data area for defaults All the RO Data type sections which do not define section names with C source are assigned in this area.	RO Data	FLASH	FLASH

Table 6.2 Sections to be Used (2/2)

Area Name	Description	Type	Loading Area	Execution Area
VECTOR_MIRROR_ TABLE	Exception processing vector table (Section to transfer data to large-capacity on-chip RAM)	Code	FLASH	LRAM
CODE_HANDLER_ JMPTBL	Program code area for user-defined functions of IRQ interrupt handler	Code	FLASH	LRAM
CODE_HANDLER	Program code area of IRQ interrupt handler This area consists of the following sections. • CODE_HANDLER • IRQ_FIQ_HANDLER	Code	FLASH	LRAM
DATA_HANDLER_ JMPTBL	Registration table data area for user-defined functions of IRQ interrupt handler	RW Data	FLASH	LRAM
ARM_LIB_STACK	Application stack area	ZI Data	-	LRAM
IRQ_STACK	IRQ mode stack area	ZI Data	-	LRAM
FIQ_STACK	FIQ mode stack area	ZI Data	-	LRAM
SVC_STACK	Supervisor (SVC) mode stack area	ZI Data	-	LRAM
ABT_STACK	Abort (ABT) mode stack area	ZI Data	-	LRAM
TTB	MMU translation table area	ZI Data	-	LRAM
ARM_LIB_HEAP	Application heap area	ZI Data	-	LRAM
DATA	Data area with initial value for defaults All the RW Data type sections which do not define section names with C source are assigned in this area.	RW Data	FLASH	LRAM
BSS	Data area without initial value for defaults All the ZI Data type sections which do not define section names with C source area assigned in this area.	ZI Data	-	LRAM

Notes: 1. "FLASH" and "LRAM" shown in Loading Area and Execution Area indicate the NOR flash memory area and the large-capacity on-chip RAM area respectively.

2. Basically the section name is set to be the same as the region's, however it consists of some sections in the areas of RESET_HANDLER, InRoot, CODE_FPU_INIT, CODE_RESET, CODE, CONST, CODE_HANDLER, DATA, and BSS. Refer to the ARM compiler toolchain manual about the region and the section.

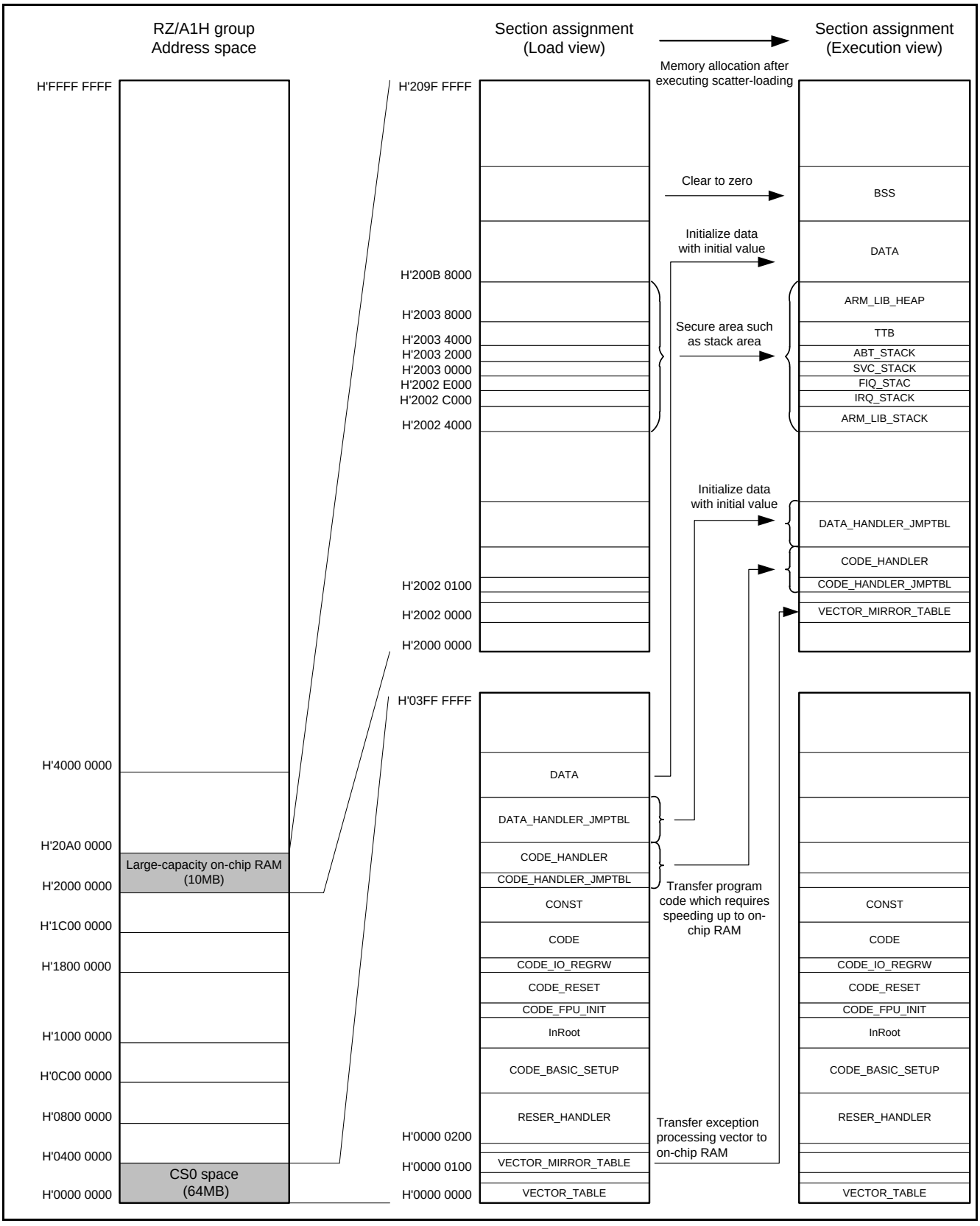

Figure 6.6 Section Assignment

6.2.2 Setting for MMU

The MMU is set to manage the 4 GB area in 1MB unit from the address H'0000 0000 in response to the memory map of the hardware resource used for the GENMAI Board. (Set by the ttb_init.s file.) The minimum unit should be 1MB when customizing the MMU based on the system.

Table 6.3 lists the Setting for MMU.

Table 6.3 Setting for MMU

Definition Name	Contents	Address	Size	Memory Type
M_SIZE_NOR	CS0 and CS1 spaces (NOR flash memory)	H'0000 0000 to H'07FF FFFF	128MB	L1 cache enable, Normal memory
M_SIZE_SDRAM	CS2 and CS3 spaces (SDRAM)	H'0800 0000 to H'0FFF FFFF	128MB	L1 cache enable, Normal memory
M_SIZE_CS45	CS4 and CS5 spaces	H'1000 0000 to H'17FF FFFF	128MB	Strongly-ordered memory (L1 cache disable)
M_SIZE_SPI	SPI multi IO bus space 1 and 2 (serial flash memory)	H'1800 0000 to H'1FFF FFFF	128MB	L1 cache enable, Normal memory
M_SIZE_RAM	Large-capacity on-chip RAM space	H'2000 0000 to H'209F FFFF	10MB	L1 cache enable, Normal memory
M_SIZE_IO_1	On-chip peripheral module and reserved area	H'20A0 0000 to H'3FFF FFFF	502MB	Strongly-ordered memory (L1 cache disable)
M_SIZE_NOR_M	CS0 and CS1 mirror spaces	H'4000 0000 to H'47FF FFFF	128MB	L1 cache disable, Normal memory
M_SIZE_SDRAM_M	CS2 and CS3 mirror spaces	H'4800 0000 to H'4FFF FFFF	128MB	L1 cache disable, Normal memory
M_SIZE_CS45_M	CS4 and CS5 mirror spaces	H'5000 0000 to H'57FF FFFF	128MB	Strongly-ordered memory (L1 cache disable)
M_SIZE_SPI_M	SPI multi IO bus mirror space 1 and 2	H'5800 0000 to H'5FFF FFFF	128MB	L1 cache disable, Normal memory
M_SIZE_RAM_M	Large-capacity on-chip RAM mirror space	H'6000 0000 to H'609F FFFF	10MB	L1 cache disable, Normal memory
M_SIZE_IO_2	On-chip peripheral module and reserved area	H'60A0 0000 to H'FFFF FFFF	2550MB	Strongly-ordered memory (L1 cache disable)

6.2.3 Exception Processing Vector Table

The RZ/A1H has seven types of exception processing (reset, undefined instruction, software interrupt, prefetch abort, data abort, IRQ, and FIQ). In the case of boot mode 0, the exception processing vector table is assigned to the area from H'0000 0000 to the area of 32 bytes (from H'0000 0000 to H'0000 001F) after the reset cancellation.

Figure 6.7 shows the contents of the sample code exception processing vector table as a description example.

```
vector_table
  LDR pc, =reset_handler       ; 0x0000_0000 : Reset exception
  LDR pc, =undefined_handler   ; 0x0000_0004 : Undefined instructions exception
  LDR pc, =svc_handler         ; 0x0000_0008 : Software interrupts exceptions
  LDR pc, =prefetch_handler    ; 0x0000_000c : Prefetch abort exception
  LDR pc, =abort_handler       ; 0x0000_0010 : Data abort exception
  LDR pc, =reserved_handler    ; 0x0000_0014 : Reserved
  LDR pc, =irq_handler         ; 0x0000_0018 : IRQ exception
  LDR pc, =fiq_handler         ; 0x0000_001c : FIQ exception
```

Figure 6.7 Description Example of Exception Processing Vector Table

6.3 Interrupt

Table 6.4 shows Interrupts using by sample code.

Table 6.4 Interrupts using by sample code

Interrupt (Source ID)	Priority	Summary
JEDI	JCU_INT_PRI(=2)	Compression/Decompression process.
JDTI	JCU_INT_PRI(=2)	Data transfer process

6.4 Basic Types

Table 6.5 lists the Basic types using in the example code.

Table 6.5 Basic types using in the example code

Symbol	Description
char_t	8-bit character
bool_t	Logical data type. The value is true (1) or false (0).
int_t	The signed integer for this library is a 32-bit signed integer.
int8_t	8-bit signed integer
int16_t	16-bit signed integer
int32_t	32-bit signed integer
uint8_t	8-bit unsigned integer
uint16_t	16-bit unsigned integer
uint32_t	32-bit unsigned integer
int_fast32_t	Fastest 32-bit minimum-width signed integer
uint_fast8_t	Fastest 8-bit minimum-width unsigned integer
uint_fast16_t	Fastest 16-bit minimum-width unsigned integer
uint_fast32_t	Fastest 32-bit minimum-width unsigned integer
uintptr_t	Same as pointer bit width unsigned integer as physical address
size_t	Same as pointer bit width unsigned integer as byte size
bit_flags_fast32_t	Same as uint_fast32_t bit flags (bit field)
bit_flags32_t	Same as uint32_t bit flags (bit field)

6.5 Constants, Enumerations and Error code

Table 6.6 Constants Used in Sample Code

Section	Symbol	Description
6.5.1	-	Version information.
6.5.2	errnum_t	Error information.
6.5.3	jcu_errorcode_t	Error code. 0 = No error.
6.5.4	jcu_codec_t	Mode selection (Compression or De-compression).
6.5.5	jcu_continue_type_t	Paused factor(continue mode).
6.5.6	jcu_detail_error_t	Error classification of the JCU driver.
6.5.7	jcu_int_detail_error_t	Particular error code.
6.5.8	jcu_swap_t	Swap setting.
6.5.9	jcu_sub_sampling_t	Sub sample of the decoded image data.
6.5.10	jcu_decode_format_t	Output pixel format of RAW image data.
6.5.11	jcu_jpeg_format_t	Pixel format of JPEG image data.
6.5.12	jcu_huff_t	Type of Huffman table (AC or DC).
6.5.13	jcu_table_no_t	Quantization table number or Huffman table number.
6.5.14	jcu_status_information_t	Internal state of the JCU driver.
6.5.15	jcu_codec_status_t	Mode selection information.
6.5.16	jcu_cbcr_offset_t	Cb/Cr range setting. If the pixel format isn't YCbCr, the JCU_CBCR_OFFSET_0 must be used.
6.5.17	jcu_interrupt_line_t	The kind of interrupt as the bit flag value.
6.5.18	jcu_interrupt_lines_t	Logical sum of the bit flag value jcu_interrupt_line_t.

6.5.1 Version

Table 6.7 Version information

Symbol	Value	Description
JCU_VERSION	-	JCU version number.
JCU_VERSION_STRING	-	Character string of the JCU version number.

6.5.2 errnum_t

Table 6.8 Error information

Symbol	Value	Description
-	0	No error.
E_OTHERS	1	Other error.
E_FEW_ARRAY	2	The fixed-length arrays size is smaller than data size.
E_FEW_MEMORY	3	Out of memory error occurred when ensure a heap memory.
E_FIFO_OVER	4	Error when enqueue to FIFO is failed.
E_NOT_FOUND_SYMBOL	5	Error of undefined symbol.
E_NO_NEXT	6	There are no next.
E_ACCESS_DENIED	7	Error of Read/Write denied.
E_NOT_IMPLEMENT_YET	9	Not implemented.
E_ERRNO	0x0E(=14)	See "errno".
E_LIMITATION	0x0F(=15)	Limitation.
E_STATE	0x10(=16)	The error which can't be executed in this state
E_PATH_NOT_FOUND	0x12(=18)	The error by which a file and a folder aren't found.

Symbol	Value	Description
E_BAD_COMMAND_ID	0x16(=22)	The command id number is the outside of the range.
E_TIME_OUT	0x17(=23)	Time out
E_NO_DEBUG_TLS	0x1D(=29)	There is no debug/work area. See 'R_OSPL_SET_DEBUG_WORK'
E_EXIT_TEST	0x1E(=30)	Stop request of the test.

6.5.3 jcu_errorcode_t

Table 6.9 Error code

Symbol	Value	Description
JCU_ERROR_OK	0x0000	No error has occurred.
JCU_ERROR_PARAM	0x4501	A parameter provided to a function is incorrect.
JCU_ERROR_STATUS	0x4502	A function was called in an incorrect state.
JCU_ERROR_CODEC_TYPE	0x4503	A function was called in an incorrect mode.
JCU_ERROR_LIMITATION	0x4504	Restrictions on JCU driver.

6.5.4 jcu_codec_t

Table 6.10 Mode selection (Compression or De-compression).

Symbol	Value	Description
JCU_ENCODE	0	Compression process.
JCU_DECODE	1	De-compression process.

6.5.5 jcu_continue_type_t

Table 6.11 Paused factor(continue mode).

Symbol	Value	Description
JCU_INPUT_BUFFER	0	Resumes reading input image data.
JCU_OUTPUT_BUFFER	1	Resumes writing output image data.
JCU_GET_IMAGE_INFO	2	Clears the process-stopped state caused by requests to read the image information.

6.5.6 jcu_detail_error_t

Table 6.12 Error classification of the JCU driver.

Symbol	Value	Description
JCU_JCDERR_OK	0x0000	Normal.
JCU_JCDERR_SOI_NOT_FOUND	0x4521	SOI not detected: SOI not detected until EOI detected.
JCU_JCDERR_INVALID_SOF	0x4522	SOF1 to SOFF detected.
JCU_JCDERR_UNPROVIDED_SOF	0x4523	Unprovided pixel format detected.
JCU_JCDERR_SOF_ACCURACY	0x4524	SOF accuracy error: Other than 8 detected.
JCU_JCDERR_DQT_ACCURACY	0x4525	DQT accuracy error: Other than 0 detected.
JCU_JCDERR_COMPONENT_1	0x4526	Component error 1: The number of SOF0 header components detected is other than 1, 3, or 4.
JCU_JCDERR_COMPONENT_2	0x4527	Component error 2: The number of components differs between SOF0 header and SOS.
JCU_JCDERR_NO_SOF0_DQT_DHT	0x4528	SOF0, DQT, and DHT not detected when SOS detected.
JCU_JCDERR_SOS_NOT_FOUND	0x4529	SOS not detected: SOS not detected until EOI detected.
JCU_JCDERR_EOI_NOT_FOUND	0x452A	EOI not detected (default).

Symbol	Value	Description
JCU_JCDERR_RESTART_INTERVAL	0x452B	Restart interval data number error detected.
JCU_JCDERR_IMAGE_SIZE	0x452C	Image size error detected.
JCU_JCDERR_LAST_MCU_DATA	0x452D	Last MCU data number error detected.
JCU_JCDERR_BLOCK_DATA	0x452E	Block data number error detected.

6.5.7 jcu_int_detail_error_t

Table 6.13 Particular error code.

Symbol	Value	Description
JCU_INT_ERROR_RESTART_INTERVAL_DATA	0x80	The number of data in the restart interval of the Huffman-coding segment is not correct in de-compression.
JCU_INT_ERROR_SEGMENT_TOTAL_DATA	0x40	The total number of data in the Huffman-coding segment is not correct in de-compression.
JCU_INT_ERROR_MCU_BLOCK_DATA	0x20	The final number of MCU data in the Huffman-coding segment is not correct in de-compression.

6.5.8 jcu_swap_t

Table 6.14 Swap setting.

Symbol	Value	Description
JCU_SWAP_NONE	0x00	No swap.
JCU_SWAP_BYTE	0x01	Byte swap.
JCU_SWAP_WORD	0x02	Word swap.
JCU_SWAP_WORD_AND_BYTE	0x03	Word-byte swap.
JCU_SWAP_LONG_WORD	0x04	Longword swap.
JCU_SWAP_LONG_WORD_AND_BYTE	0x05	Longword-byte swap.
JCU_SWAP_LONG_WORD_AND_WORD	0x06	Longword-word swap.
JCU_SWAP_LONG_WORD_AND_WORD_AND_BYTE	0x07	Longword-word-byte swap.

6.5.9 jcu_sub_sampling_t

Table 6.15 Sub sample of the decoded image data.

Symbol	Value	Description
JCU_SUB_SAMPLING_1_1	0x00	No subsampling.
JCU_SUB_SAMPLING_1_2	0x01	Subsamples output data into 1/2.
JCU_SUB_SAMPLING_1_4	0x02	Subsamples output data into 1/4.
JCU_SUB_SAMPLING_1_8	0x03	Subsamples output data into 1/8.

6.5.10 jcu_decode_format_t

Table 6.16 Output pixel format of RAW image data.

Symbol	Value	Description
JCU_OUTPUT_YCbCr422	0x00	YCbCr422
JCU_OUTPUT_ARGB8888	0x01	ARGB8888
JCU_OUTPUT_RGB565	0x02	RGB565

6.5.11 jcu_jpeg_format_t

Table 6.17 Pixel format of JPEG image data.

Symbol	Value	Description
JCU_JPEG_YCbCr444	0x00	YCbCr444
JCU_JPEG_YCbCr422	0x01	YCbCr422
JCU_JPEG_YCbCr420	0x02	YCbCr420
JCU_JPEG_YCbCr411	0x06	YCbCr411

6.5.12 jcu_huff_t

Table 6.18 Type of Huffman table (AC or DC).

Symbol	Value	Description
JCU_HUFFMAN_AC	0x00	AC
JCU_HUFFMAN_DC	0x01	DC

6.5.13 jcu_table_no_t

Table 6.19 Quantization table number or Huffman table number.

Symbol	Value	Description
JCU_TABLE_NO_0	0x00	Quantization table No. 0 (JCQTBL0), or DC/AC Huffman table No. 0 (JCHTBD0 / JCHTBA0)
JCU_TABLE_NO_1	0x01	Quantization table No. 1 (JCQTBL1), or DC/AC Huffman table No. 1 (JCHTBD1 / JCHTBA1)
JCU_TABLE_NO_2	0x02	Quantization table No. 2 (JCQTBL2)
JCU_TABLE_NO_3	0x03	Quantization table No. 3 (JCQTBL3)

6.5.14 jcu_status_information_t

Table 6.20 Internal state of the JCU driver.

Symbol	Value	Description
JCU_STATUS_UNDEF	0x00	The JCU is uninitialized status.
JCU_STATUS_INIT	0x01	The JCU is initialized status.
JCU_STATUS_SELECTED	0x02	The JCU mode is selected.
JCU_STATUS_READY	0x08	The JCU decode/encode is ready, or the JCU decode/encode has been completed.
JCU_STATUS_RUN	0x10	The JCU decode/encode being executed.
JCU_STATUS_INTERRUPTING	0x40	The state that interrupt occurred.
JCU_STATUS_INTERRUPTED	0x80	The state after interrupt function executed.

6.5.15 jcu_codec_status_t

Table 6.21 Mode selection information.

Symbol	Value	Description
JCU_CODEC_NOT_SELECTED	-1	The state of the JCU mode is not selected.
JCU_STATUS_ENCODE	0	The state of the JCU mode is JCU_ENCODE.
JCU_STATUS_DECODE	1	The state of the JCU mode is JCU_DECODE.

6.5.16 jcu_cbcr_offset_t

Table 6.22 Cb/Cr range setting.

Symbol	Value	Description
JCU_CBCR_OFFSET_0	0	Range from -128 to 127
JCU_CBCR_OFFSET_128	1	Range from 0 to 255

If the pixel format isn't YCbCr, the JCU_CBCR_OFFSET_0 must be used.

6.5.17 jcu_interrupt_line_t

Table 6.23 The kind of interrupt as the bit flag value.

Symbol	Value	Description
JCU_INTERRUPT_LINE_JEDI	0x00000001u	Interrupt of JEDI.
JCU_INTERRUPT_LINE_JDTI	0x00000002u	Interrupt of JDTI.

6.5.18 jcu_interrupt_lines_t

Table 6.24 Logical sum of the bit flag value jcu_interrupt_line_t.

Symbol	Value	Description
JCU_INTERRUPT_LINE_ALL	0x00000003u	Interrupt of both of JEDI and JDTI.

6.6 Structures

Table 6.25 Structures

Section	Symbol	Outline
6.6.1	jcu_count_mode_param_t	Parameters for the count mode(division process).
6.6.2	jcu_buffer_t	Structure for the input/output buffer setting.
6.6.3	jcu_buffer_param_t	Parameters for the input/output buffer setting in de-compression.
6.6.4	jcu_decode_param_t	Parameters for the option setting in de-compression.
6.6.5	jcu_image_info_t	Structure for the image information of the decoded JPEG data.
6.6.6	jcu_encode_param_t	Parameters for the option setting in compression.
6.6.7	jcu_internal_information_t	Structure for the internal state of the JCU driver.
-	jcu_async_status_t	The JCU driver state and interrupt status.

6.6.1 jcu_count_mode_param_t

jcu_count_mode_param_t

Synopsis	Parameters for the count mode(division process).	
Header	r_jcu_api.h	
Description		
Member variable	bool_t inputBuffer.isEnable	false: Disable the division processing on input buffer. true: Enable the division processing on input buffer.
	bool_t inputBuffer.isInitAddress	false: When decoding paused, the input address isn't initialized. true: When decoding paused, the input address is initialized by “inputBuffer.restartAddress”.
	uint32_t* inputBuffer.restartAddress	If "IsInitAddress" is "true", the input data address is initialized by this value.
	uint32_t inputBuffer.dataCount	The division size of the input buffer. In the case of decoding mode, when data of "dataCount" byte count is input to JCU, it pauses. In the case of encoding mode, when data of "dataCount" line count is input to JCU, it pauses. The “dataCount” must be a multiple of 8 bytes.
	bool_t outputBuffer.isEnable	false: Disable the division processing on output buffer. true: Enable the division processing on output buffer.
	bool_t outputBuffer.isInitAddress	false: When decoding paused, the input address isn't initialized. true: When decoding paused, the output address is initialized by “outputBuffer.restartAddress”.
	uint32_t* outputBuffer.restartAddress	If "IsInitAddress" is "true", the output data address is initialized by this value.
	uint32_t outputBuffer.dataCount	The division size of the output buffer. In the case of decoding mode, when JCU outputs data of "dataCount" line (when data of YCbCr420 was decoded, two times of "dataCount" lines), it pauses. In the case of encoding mode, when JCU outputs data of "dataCount" byte, it pauses. The “dataCount” must be a multiple of 8 lines.

6.6.2 jcu_buffer_t

jcu_buffer_t

Synopsis	Structure for the input/output buffer setting.	
Header	r_jcu_api.h	
Description		
Member variable	jcu_swap_t swapSetting	Byte/Word/Longword Swap.
	uint32_t* address	Buffer address.

6.6.3 jcu_buffer_param_t

jcu_buffer_param_t

Synopsis	Parameters for the input/output buffer setting in de-compression.	
Header	r_jcu_api.h	
Description		
Member variable	jcu_buffer_t source	Input buffer.
	jcu_buffer_t destination	Output buffer.
	int16_t lineOffset	Line offset.

6.6.4 jcu_decode_param_t

jcu_decode_param_t

Synopsis	Parameters for the option setting in de-compression.	
Header	r_jcu_api.h	
Description		
Member variable	jcu_sub_sampling_t verticalSubSampling	Vertical subsampling.
	jcu_sub_sampling_t horizontalSubSampling	Horizontal subsampling.
	jcu_decode_format_t decodeFormat	The output pixel format of RAW image data.
	jcu_cbcr_offset_t outputCbCrOffset	Cb/Cr range setting. If the pixel format isn't YCbCr, the Cb/Cr value has to be JCU_CBCR_OFFSET_0.
	uint8_t alpha	Alpha value setting. If the pixel format isn't ARGB8888, the alpha value has to be zero.

6.6.5 jcu_image_info_t

jcu_image_info_t

Synopsis	Structure for the image information of the decoded JPEG data.	
Header	r_jcu_api.h	
Description		
Member variable	uint32_t width	The width of the image data.
	uint32_t height	The height of the image data
	jcu_jpeg_format_t encodedFormat	The pixel format of original JPEG data.

6.6.6 jcu_encode_param_t

jcu_encode_param_t

Synopsis	Parameters for the option setting in compression.	
Header	r_jcu_api.h	
Description		
Member variable	jcu_jpeg_format_t encodeFormat	The pixel format of compressed JPEG data. This value has to be JCU_JPEG_YCbCr422.
	int32_t QuantizationTable[]	Quantization table.
	int32_t HuffmanTable[]	Huffman table.
	uint32_t DRI_value	DRI(Define Restart Interval) value.

	uint32_t width	The width of the input image data.
	uint32_t height	The height of the input image data
	jcu_cbcr_offset_t inputCbCrOffset	Cb/Cr range setting.

6.6.7 jcu_internal_information_t

jcu_internal_information_t		
Synopsis	Structure for the internal state of the JCU driver.	
Header	r_jcu_api.h	
Description		
Member variable	jcu_status_information_t status	The internal state of the JCU driver.
	jcu_codec_status_t codec	Mode selection information.
	bool_t isRunning	false: JCU driver inactive. true: JCU driver active.
	bool_t isPaused	false: JCU driver is not paused true: JCU driver is paused.
	bool_t isCountMode	false: JCU driver is not count mode. true: JCU driver is count mode.

6.7 List of Variables

Table 6.26 Global Variables

Type	Variable Name	Contents	Function Used
jcu_internal_information_t	g_jcu_condition	The internal state of the JCU driver.	All API functions, other than “R_JCU_GetEncodedSize”.
			“JCU_CheckInterruptSource” function.

6.8 List of Functions

Table 6.27 API functions

Section	Function Name	Outline
6.9.1	R_JCU_Initialize	Initializes the JCU driver.
6.9.2	R_JCU_Terminate	Performs termination processing for the JCU driver(synchronous process).
6.9.3	R_JCU_TerminateAsync	Performs termination processing for the JCU driver(asynchronous process).
6.9.4	R_JCU_SelectCodec	Sets the JCU mode.
6.9.5	R_JCU_SetCountMode	Sets the count mode.
6.9.6	R_JCU_SetPauseForImageInfo	When the image information can be acquired, it's made the setting which is paused.
6.9.7	R_JCU_SetErrorFilter	The particular error code(jcu_int_detail_error_t) was set to valid.
6.9.8	R_JCU_Start	Starts JCU process(synchronous process).
6.9.9	R_JCU_StartAsync	Starts JCU process(asynchronous process).
6.9.10	R_JCU_Continue	Resume the JCU process(synchronous process).
6.9.11	R_JCU_ContinueAsync	Resume the JCU process(asynchronous process).
6.9.12	R_JCU_SetDecodeParam	Sets decoding parameter.
6.9.13	R_JCU_GetImageInfo	Gets information on the JPEG data.
6.9.14	R_JCU_SetEncodeParam	Sets encoding parameter.
6.9.15	R_JCU_SetQuantizationTable	Sets the Quantization table.
6.9.16	R_JCU_SetHuffmanTable	Sets the Huffman table.
6.9.17	R_JCU_GetEncodedSize	Gets the size of data to be compressed.
6.9.18	R_JCU_GetAsyncStatus	Gets the pointer of a structure that indicates the state of the interrupt and asynchronous process.
6.9.19	R_JCU_OnInterrupting	Interrupt is accepted.
6.9.20	R_JCU_OnInterrupted	Interrupt function is executed.

Table 6.28 User defined functions

Section	Function Name	Outline
6.9.21	R_JCU_OnInitialize	Initializes the user defined process.
6.9.22	R_JCU_OnFinalize	Finalizes the user defined process.
6.9.23	R_JCU_SetDefaultAsync	Sets the default value of the variable of r_ospl_async_t type structure.
6.9.24	R_JCU_SetInterruptCallbackCaller	The object which the interrupt callback function is called is registered with driver's transplantation layer.
6.9.25	R_JCU_OnEnableInterrupt	It's made interrupt enabled.
6.9.26	R_JCU_OnDisableInterrupt	It's made interrupt disabled.

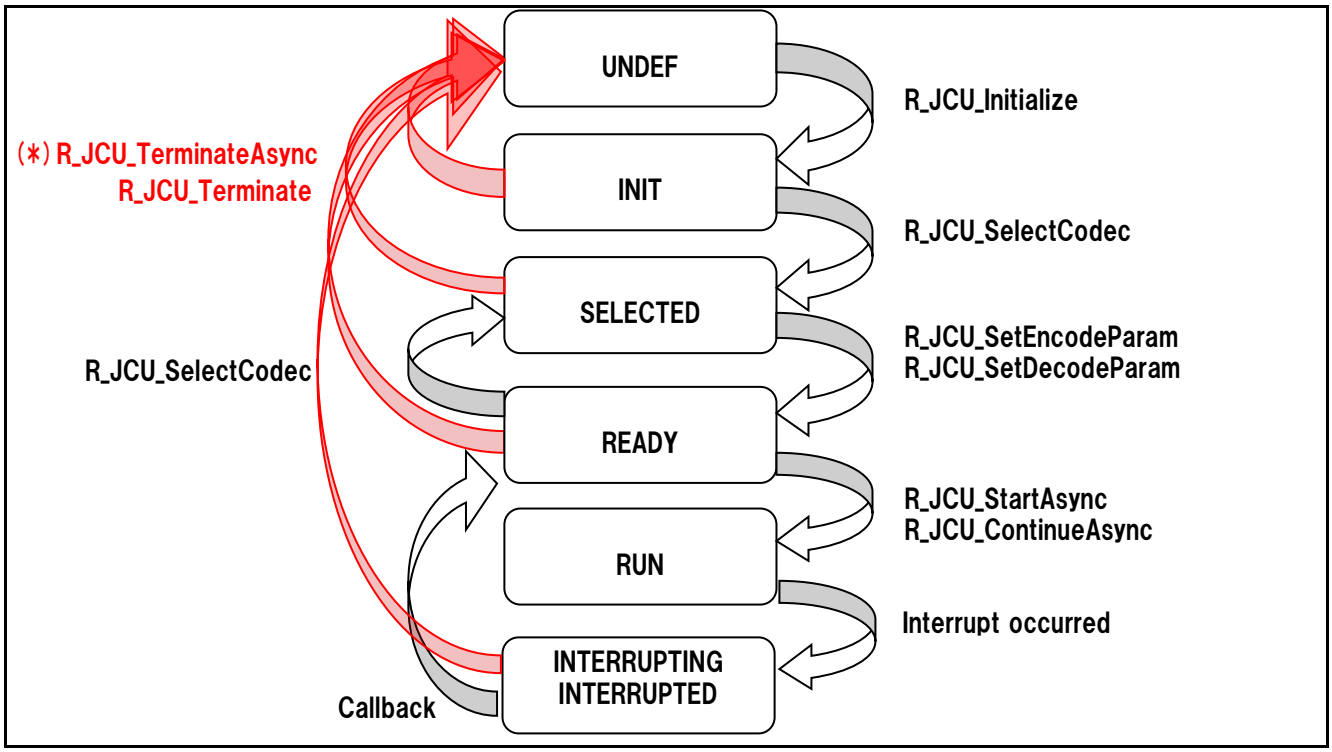

Figure 6.8 State transition diagram.

(*)When executing R_JCU_TerminateAsync in the Run state, the state doesn't transfer. After interrupt occurred(the callback function is executed), the state transfers to “Undef”.

Table 6.29 State transition table

API	Status						Transition destination
	Undef	Init	Selected	Ready	Run	Interrupting Interrupted	
R_JCU_Initialize	OK	NG	NG	NG	NG	NG	Init
R_JCU_Terminate	OK	OK	OK	OK	OK	OK	Undef
R_JCU_TerminateAsync	OK	OK	OK	OK	OK*	OK	Undef
R_JCU_SelectCodec	NG	OK	OK	OK	NG	NG	Selected
R_JCU_SetCountMode	NG	NG	OK	OK	NG	NG	No change
R_JCU_SetPauseFor ImageInfo	NG	NG	OK	OK	NG	NG	No change
R_JCU_SetErrorFilter	OK	OK	OK	OK	OK	OK	No change
R_JCU_Start	NG	NG	NG	OK	NG	NG	No change
R_JCU_StartAsync	NG	NG	NG	OK	NG	NG	Run
R_JCU_Continue	NG	NG	NG	OK	NG	NG	No change
R_JCU_ContinueAsync	NG	NG	NG	OK	NG	NG	Run
R_JCU_SetDecodeParam	NG	NG	OK	OK	NG	NG	Ready
R_JCU_GetImageInfo	NG	NG	NG	OK	NG	NG	No change
R_JCU_SetEncodeParam	NG	NG	OK	OK	NG	NG	Ready
R_JCU_SetQuantizationTable	NG	NG	OK	OK	NG	NG	No change
R_JCU_SetHuffmanTable	NG	NG	OK	OK	NG	NG	No change
R_JCU_GetEncodedSize	NG	NG	NG	OK	NG	NG	No change
R_JCU_GetAsyncStatus	OK	OK	OK	OK	OK	OK	No change

(*)When executing R_JCU_TerminateAsync in the Run state, the state doesn't transfer. After interrupt occurred(the callback function is executed), the state transfers to “Undef”.

6.9 Description of function

The specification of sample code functions is following below:

6.9.1 R_JCU_Initialize

Synopsis	Initializes the JCU driver.
Header	r_jcu_api.h
Declaration	jcu_errorcode_t R_JCU_Initialize (void* const NullConfig);
Description	The state will be in the initialized status. Initializes the internal status(g_jcu_condition). The user defined function(R_JCU_OnInitialize) is called. Perform the following processing in the user defined function. 1. Clock supply to JCU. 2. Sets the priority of interrupt. 3. Sets the environment-depend process.
Arguments	void *const NullConfig Please set a null.
Return value	Error code.

6.9.2 R_JCU_Terminate

Synopsis	Performs termination processing for the JCU driver (synchronous process).
Header	r_jcu_api.h
Declaration	jcu_errorcode_t R_JCU_Terminate(void);
Description	The processing which finishes a JCU driver. The function keeps executing until processing ends. The state will be in the uninitialized status. The user defined function(R_JCU_OnFinalize) is called. Perform the following processing in the user defined function. 1. Clock stopped to JCU. 2. Clear the priority of interrupt. 3. Sets the environment-depend process. When “g_jcu_condition.status” is “JCU_STATUS_RUN”, it waits until processing ends.
Arguments	None
Return value	Error code.

6.9.3 R_JCU_TerminateAsync

Synopsis	Performs termination processing for the JCU driver (asynchronous process).
Header	r_jcu_api.h
Declaration	jcu_errorcode_t R_JCU_TerminateAsync(r_ospl_async_t* const async);
Description	The processing which finishes a JCU driver. The function which is executing is suspended before processing ends. For the detail of the argument ‘async’, see the explanation of R_DRIVER_TransferAsync function in OS transplantation layer OSPL for RZ/A1H group PFV,JCU (R01AN1887EJ). About others, see R_JCU_Terminate.
Arguments	r_ospl_async_t* const async Synchronization setting.
Return value	Error code.

6.9.4 R_JCU_SelectCodec

Synopsis	Sets the JCU mode.	
Header	r_jcu_api.h	
Declaration	jcu_errorcode_t R_JCU_SelectCodec(const jcu_codec_t codec);	
Description	This function selects the JCU mode(Compression or De-compression). Please set again all parameters of decode, encode and count mode. Because when this function was called, these parameters were initialized.	
Arguments	const jcu_codec_t codec	JCU mode(Compression or De-compression)
Return value	Error code.	

6.9.5 R_JCU_SetCountMode

Synopsis	Sets the count mode.	
Header	r_jcu_api.h	
Declaration	jcu_errorcode_t R_JCU_SetCountMode(const jcu_count_mode_param_t *const buffer);	
Description	Sets the count mode(division process). The division processing on input buffer can't be used simultaneously with the division processing on output buffer.	
Arguments	const jcu_count_mode_param_t *const buffer	Count mode
Return value	Error code.	

6.9.6 R_JCU_SetPauseForImageInfo

Synopsis	When the image information can be acquired, it's made the setting which is paused.	
Header	r_jcu_api.h	
Declaration	jcu_errorcode_t R_JCU_SetPauseForImageInfo(const bool_t is_pause)	
Description	When the image information can be acquired, it's made the setting which is paused by the R_JCU_GetImageInfo function.	
Arguments	const bool_t is_pause	TRUE: It's made the setting which is paused. FALSE: It's made the setting which isn't paused.
Return value	Error code.	

6.9.7 R_JCU_SetErrorFilter

Synopsis	The particular error code(jcu_int_detail_error_t) was set to valid.	
Header	r_jcu_api.h	
Declaration	jcu_errorcode_t R_JCU_SetErrorFilter(jcu_int_detail_errors_t filter);	
Description	The particular error code was set to valid. When the valid decoding error occurred, interrupt occurs.	
Arguments	jcu_int_detail_errors_t filter	The valid decoding error code(jcu_int_detail_error_t) as the bit flag value.
Return value	Error code.	

6.9.8 R_JCU_Start

Synopsis	Starts JCU process (synchronous process).
Header	r_jcu_api.h
Declaration	jcu_errorcode_t R_JCU_Start(void);
Description	Starts JCU process. The function will not return until decoding or encoding ends or

	pauses. Using the R_JCU_SetDecoderParam API function or the R_JCU_SetEncoderParamSet API function, set the parameters before the JCU process starts You cannot stop the JCU process, after the JCU process starts.
Arguments	None.
Return value	Error code.

6.9.9 R_JCU_StartAsync

Synopsis	Starts JCU process (asynchronous process).
Header	r_jcu_api.h
Declaration	jcu_errorcode_t R_JCU_StartAsync(r_ospl_async_t* const async);
Description	Starts JCU process. The function will return before decoding or encoding ends or pauses. For the detail of the argument 'async', see the explanation of R_DRIVER_TransferAsync function in OS transplantation layer OSPL for RZ/A1H group PFV,JCU (R01AN1887EJ). About others, see R_JCU_Start.
Arguments	r_ospl_async_t* const async — Synchronization setting.
Return value	Error code.

6.9.10 R_JCU_Continue

Synopsis	Resume the JCU process (synchronous process).
Header	r_jcu_api.h
Declaration	jcu_errorcode_t R_JCU_Continue(const jcu_continue_type_t type);
Description	Processing of JCU which paused is resumed. The function will not return until decoding or encoding ends or pauses. The parameter is a paused factor.
Arguments	const jcu_continue_type_t type — Paused factor(continue mode)
Return value	Error code.

6.9.11 R_JCU_ContinuetAsync

Synopsis	Resume the JCU process (asynchronous process).
Header	r_jcu_api.h
Declaration	jcu_errorcode_t R_JCU_ContinueAsync(const jcu_continue_type_t type, r_ospl_async_t* const async);
Description	Starts JCU process. The function will return before decoding or encoding ends or pauses. About others, see R_JCU_Start.
Arguments	r_ospl_async_t* const async — Synchronization setting.
Return value	Error code.

6.9.12 R_JCU_SetDecodeParam

Synopsis	Sets decoding parameter.	
Header	r_jcu_api.h	
Declaration	jcu_errorcode_t R_JCU_SetDecodeParam(const jcu_decode_param_t *const decode, const jcu_buffer_param_t *const buffer, const uint32_t interruptKind);	
Description	Sets decoding parameter. If the pixel format isn't ARGB8888, the alpha value has to be zero. If the pixel format isn't YCbCr, the Cb/Cr value has to be JCU_CBCR_OFFSET_0.	
Arguments	const jcu_decode_param_t *const decode	Pointer to variable of decode parameter information.
	const jcu_buffer_param_t *const buffer	Pointer to variable of buffer.
Return value	Error code.	

6.9.13 R_JCU_GetImageInfo

Synopsis	Gets information on the JPEG data.	
Header	r_jcu_api.h	
Declaration	jcu_errorcode_t R_JCU_GetImageInfo(jcu_image_info_t *const buffer);	
Description	Gets the image information(width, height, pixel format) of the decoded JPEG data. If data is read before the request which reads the image information, the data is not guaranteed. If the pixel format of the decoded JPEG data is outside of the jcu_jpeg_format_t, it's the error, so JCU can't decode.	
Arguments	jcu_image_info_t *const buffer	Pointer to variable of image information.
Return value	Error code.	

6.9.14 R_JCU_SetEncodeParam

Synopsis	Sets encoding parameter.	
Header	r_jcu_api.h	
Declaration	jcu_errorcode_t R_JCU_SetEncodeParam(const jcu_encode_param_t *const encode, const jcu_buffer_param_t *const buffer, const uint32_t interruptKind);	
Description	Sets Encoding parameter.	
Arguments	const jcu_encode_param_t *const encode	Pointer to variable of encode parameter information.
	const jcu_buffer_param_t *const buffer	Pointer to variable of buffer.
Return value	Error code.	

6.9.15 R_JCU_SetQuantizationTable

Synopsis	Sets the Quantization table.	
Header	r_jcu_api.h	
Declaration	jcu_errorcode_t R_JCU_SetQuantizationTable(const jcu_table_no_t tableNo, const uint8_t *const table);	
Description	Quantization table data. For the setting value of the quantization table data, see "RZ/A1H Group User's Manual:Hardware" section 45.3.1 (4), (a) Quantization Table Specification.	
Arguments	const jcu_table_no_t	Quantization table number.

	tableNo	
	const uint8_t *const table	Quantization table.
Return value	Error code.	

6.9.16 R_JCU_SetHuffmanTable

Synopsis	Sets the Huffman table.	
Header	r_jcu_api.h	
Declaration	jcu_errorcode_t R_JCU_SetHuffmanTable(const jcu_table_no_t tableNo, const jcu_huff_t type, const uint8_t *const table);	
Description	Huffman table data. For the setting value of the Huffman table data, see "RZ/A1H Group User's Manual:Hardware" section 45.3.1 (4), (b) Huffman Table Specification.	
Arguments	const jcu_table_no_t tableNo	Huffman table number.
	const jcu_huff_t type	Type of Huffman table (AC or DC).
	const uint8_t *const table	Huffman table
Return value	Error code.	

6.9.17 R_JCU_GetEncodedSize

Synopsis	Gets the size of data to be compressed.	
Header	r_jcu_api.h	
Declaration	jcu_errorcode_t R_JCU_GetEncodedSize(size_t *const out_Size);	
Description	Gets the size of data to be compressed. If data is read before interrupt of encoding complete, the data is not guaranteed.	
Arguments	size_t *const out_Size	Pointer to variable of the data size.
Return value	Error code.	

6.9.18 R_JCU_GetAsyncStatus

Synopsis	Gets the pointer of a structure that indicates the state of the interrupt and asynchronous process.	
Header	r_jcu_api.h	
Declaration	R_JCU_GetAsyncStatus(const jcu_async_status_t** const out_Status)	
Description	Pointer variable ‘out_Status’ needs the const modifiers.	
Arguments	jcu_async_status_t** out_Status	(Output) Pointer of a structure that indicates the state of the interrupt and asynchronous process.
Return value	Error code. No error = 0.	

6.9.19 R_JCU_OnInterrupting

Synopsis	Interrupt is accepted.	
Header	r_jcu_api.h	
Declaration	errnum_t R_JCU_OnInterrupting(const r_ospl_interrupt_t* const InterruptSource);	
Description	This function is usually called automatically from the interrupt callback function of the default. This function sets the value of the interrupt status register to variable ‘gs_jcu_internal_information AsyncStatus.InterruptFlags’. And, Interrupt request is cleared after it. For the detail, see the explanation of R_DRIVER_OnInterrupting function in OS	

	transplantation layer OSPL for RZ/A1H group PFV,JCU (R01AN1887EJ).	
Arguments	r_ospl_interrupt_t* InterruptSource	Interruption sender
Return value	Error code. No error = 0.	

6.9.20 R_JCU_OnInterrupted

Synopsis	Interrupt function is executed.
Header	r_jcu_api.h
Declaration	errnum_t R_JCU_OnInterrupted(void)
Description	This function is usually called automatically from the interrupt callback function of the default. Variable ‘gs_jcu_internal_information. AsyncStatus.InterruptFlags’ the e function set in 1 is cleared in 0. And, interrupt function is executed. For the detail, see the explanation of R_DRIVER_OnInterrupted function in OS transplantation layer OSPL for RZ/A1H group PFV,JCU (R01AN1887EJ).
Arguments	None.
Return value	Error code. No error = 0.

6.9.21 R_JCU_OnInitialize

Synopsis	Initializes the user defined process.
Header	r_jcu_pl.h
Declaration	errnum_t R_JCU_OnInitialize (void);
Description	The user-defined function executed by an initializing process of the JCU driver. If necessary, execute the following processing. - Clock control - Set interrupt priority - Environment-depend process
Arguments	None.
Return value	Error code. No error = 0.

6.9.22 R_JCU_OnFinalize

Synopsis	Finalizes the user defined process.	
Header	r_jcu_pl.h	
Declaration	errnum_t R_JCU_OnFinalize (errnum_t e);	
Description	The user-defined function executed by a finalizing process of the JCU driver. If necessary, execute the following processing. - Clock stop - Clear interrupt priority - Environment-depend process	
Arguments	errnum_t e	Error code. Use it for a return value.
Return value	Error code.	

6.9.23 R_JCU_SetDefaultAsync

Synopsis	Sets the default value of the variable of r_ospl_async_t type structure.
Header	r_jcu_pl.h

Declaration	void R_JCU_SetDefaultAsync(r_ospl_async_t* const Async)	
Description	The user-defined function executed by all asynchronous process of the JCU driver. Execute the following processing. - Member of variable of r_ospl_async_t type structure corresponds to the 'Flags' is set to the default-value, if the 'Flags' member of the variable is zero. - 'ReturnValue' member of the variable is cleared by asynchronous caller's processing.	
Arguments	r_ospl_async_t* Async	Synchronization setting. 'NULL' can't be used.
Return value	Error code.	

6.9.24 R_JCU_SetInterruptCallbackCaller

Synopsis	The object which the interrupt callback function is called is registered with driver's transplantation layer.	
Header	r_jcu_pl.h	
Declaration	errnum_t R_JCU_SetInterruptCallbackCaller(const r_ospl_caller_t* const Caller)	
Description	The user-defined function executed by all asynchronous process of the JCU driver. Execute the following processing. - The setting which calls the R_OSPL_CallInterruptCallback function(value of the argument 'Caller' is designated) from the interrupt handler. For the detail, see the explanation of R_DRIVER_OnInterrupted function in OS transplantation layer OSPL for RZ/A1H group PFV,JCU (R01AN1887EJ). Driver caller's function manages the body of the structure an argument 'Caller' indicates.	
Arguments	r_ospl_caller_t* caller	The value sent to the R_OSPL_CallInterruptCallback function
Return value	Error code. No error = 0.	

6.9.25 R_JCU_OnEnableInterrupt

Synopsis	It's made interrupt enabled.	
Header	r_jcu_pl.h	
Declaration	void R_JCU_OnEnableInterrupt(jcu_interrupt_lines_t const Enables)	
Description	The user-defined function executed by the processing which enable interrupt of the JCU driver. Execute the following processing. -Interrupt-service of JCU is enabled to execute.	
Arguments	jcu_interrupt_lines_t const Enables	The kind of interrupt as the bit flag value. The flag is set to '1' for enabled interrupt.
Return value	None.	

6.9.26 R_JCU_OnDiableInterrupt

Synopsis	It's made interrupt Disabled.	
Header	r_jcu_pl.h	
Declaration	void R_JCU_OnDisableInterrupt(jcu_interrupt_lines_t const Disables)	
Description	The user-defined function executed by the processing which disable interrupt of the JCU driver. Execute the following processing. -Interrupt-service of JCU is disabled to execute.	
Arguments	jcu_interrupt_lines_t	The kind of interrupt as the bit flag value. The flag is set to

	const	Enables	'1' for disabled interrupt.
Return value	None.		

7. Sample Codes

The sample codes can be downloaded from the Renesas Electronics website.

8. Documents for Reference

User's Manual: Hardware

RZ/A1H Group User's Manual: Hardware
The latest version can be downloaded from the Renesas Electronics website.

R7S72100 RTK772100BC00000BR (GENMAI) User's Manual
The latest version can be downloaded from the Renesas Electronics website.

R7S72100 CPU (GENMAI) Optional Board RTK7721000B00000BR User's Manual
The latest version can be downloaded from the Renesas Electronics website.

ARM Architecture Reference Manual ARMv7-A and ARMv7-R edition Issue C
The latest version can be downloaded from the ARM website.

ARM Generic Interrupt Controller Architecture Specification Architecture version 1.0
The latest version can be downloaded from the ARM website.

Technical Update/Technical News

The latest information can be downloaded from the Renesas Electronics website.

User's Manual: Development Tools

ARM Software Development Tools (ARM Compiler toolchain, ARM DS-5 etc) can be downloaded from the ARM website.
The latest version can be downloaded from the Renesas Electronics website.

Website and Support

Renesas Electronics website
http://www.renesas.com

Inquiries
http://www.renesas.com/contact/

Revision History

Rev.	Date	Description	
		Page	Summary
1.00	Jun.20.2014	-	First edition issued

General Precautions in the Handling of MPU/MCU Products

The following usage notes are applicable to all MPU/MCU products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

1. Handling of Unused Pins

   Handle unused pins in accordance with the directions given under Handling of Unused Pins in the manual.

   — The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible. Unused pins should be handled as described under Handling of Unused Pins in the manual.

2. Processing at Power-on

   The state of the product is undefined at the moment when power is supplied.

   — The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the moment when power is supplied.
   In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the moment when power is supplied until the reset process is completed.
   In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the moment when power is supplied until the power reaches the level at which resetting has been specified.

3. Prohibition of Access to Reserved Addresses

   Access to reserved addresses is prohibited.

   — The reserved addresses are provided for the possible future expansion of functions. Do not access these addresses; the correct operation of LSI is not guaranteed if they are accessed.

4. Clock Signals

   After applying a reset, only release the reset line after the operating clock signal has become stable. When switching the clock signal during program execution, wait until the target clock signal has stabilized.

   — When the clock signal is generated with an external resonator (or from an external oscillator) during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Moreover, when switching to a clock signal produced with an external resonator (or by an external oscillator) while program execution is in progress, wait until the target clock signal is stable.

5. Differences between Products

   Before changing from one product to another, i.e. to a product with a different part number, confirm that the change will not lead to problems.

   — The characteristics of an MPU or MCU in the same group but having a different part number may differ in terms of the internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

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